

**AMPLIFIER ARRANGEMENT, CIRCUIT AND METHOD WITH IMPROVED
COMMON MODE REJECTION RATIO**

ABSTRACT

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A load driver circuit is provided with an improved common mode rejection ratio. A voltage regulator (150) regulates a ground voltage (120) in response to variations in input voltage (110) and provides a regulated ground voltage. An amplifier stage has a first amplifier (140) with sense inputs coupled to receive voltage signals from a sense resistor (130) of the load driver circuit. The first amplifier (140) is powered by the input voltage (110) and the regulated ground voltage respectively, such that the common mode rejection ratio of the load driver circuit is reduced. In this way an arrangement, circuit and method is provided in which CMRR is drastically improved, rendering a single (or first) stage of a current sensing load driver circuit substantially immune to common mode noise.